



U.S. UTILITY PATENT APPLICATION

SCANNED **O.I.P.E.**
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SA Q.A. A.G.

PATENT DATE

APPLICATION NO. 09/816480	CONT/PRIOR F	CLASS 703	SUBCLASS 22	ART UNIT 2123	EXAMINER SERGIANT
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Katsuyoshi Kohno
Atsushi Mizuno

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Design verification method, design verification device for microprocessors, and pipeline simulator generation device

PTO-2040
12/89

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
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